CHIP SORTER
Model CS - 10

The chip tester Model CS - 10 is designed for automatic testing and sorting of chip components depending on the measuring bridge used on the machine.
Chips are tested individually in the rotary carrier plate with a sliding contact and sorted according to preset parameters. Chips are binned into 10 bins and 1 bin for untested plus out of range chips.
Chips are transported by the rotary test plate to the test position where they are probed. Depending on the options used, the machine can have up to 5 measuring probes. The vacuum prevents chips to fall out of the test plate until they are binned in one of 11 bins.
A special design of the sliding contact ensures a reliable contacting and long life time. This contact method provides accurate measurements of different parameters including very accurate test of C and tg D testing at 1 MHz.
The automatic chips feeding is provided by the bulk feeder. An video display is available on the test bridge for continuous bin counting and information of preset tolerances.
It is also possible to preset number of chips in a bin. When this number is reached, the machine stops sorting.
Fewer moving parts of the machine ensure lower maintenance costs, reliable operation and long life time of the machine.
TECHNICAL DATA :
- Max. chip size : 4,5 x 3,2 mm
- Min. chip size : 1,6 x 0,8 mm
- Dimensions: 1000 x 700 x 1500 mm
- Power requirement :  - Electricity 220 V, 50 Hz
  - Compressed air : 6 bars, 100 l/min
  - Vacuum : 0,6 bar, 200 l/min

Option 1 :
- capacitance measurements ( instrument HP 4278 A )
- Capacity : up to 50,000 pcs / hour
Range and test accuracy :

<table>
<thead>
<tr>
<th></th>
<th>1 kHz</th>
<th>1MHz</th>
</tr>
</thead>
<tbody>
<tr>
<td>C - range</td>
<td>1000 pF - 2 uF</td>
<td>1 pF - 2048 nF</td>
</tr>
<tr>
<td>C - accuracy</td>
<td>+ ( A+0,05 pF + 0,005 % of full scale )</td>
<td></td>
</tr>
<tr>
<td></td>
<td>A = bridge accuracy</td>
<td></td>
</tr>
<tr>
<td>D - range</td>
<td>0,00000 - 0,99999</td>
<td>0,00000 - 0,05000</td>
</tr>
<tr>
<td>D - accuracy</td>
<td>+ (B=(3,15 x 10 x C in nF)         +(B+(3,15x10xCin pF)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>B= bridge accuracy</td>
<td></td>
</tr>
</tbody>
</table>

Option 2 : 
- measurements of chip VDR
- instrument : KEITHLEY 2400 ( 200 V )
  KEITHLEY 2410 ( 1100 V )
- tests :
  1. Nominal VDR voltage at specified DC current of specified duration
  2. DC standby current at specified DC voltage of specified duration
  3. Non linear exponent test at specified currents ( I1 and I2 )
     \[ \alpha = \log ( \frac{I2}{I1} ) / \log ( \frac{V2}{V1} ) \]
- Capacities :
  1 instrument
  - 35.000 pcs/hour one side measurement
  - 18.000 pcs/hour two sides measurement
  2 instruments
  - 35.000 pcs/hour two sides measurement

Option 3 :
- measurements of chip NTC or chip PTC
- resistance measurements ( instrument HP 34401 MULTIMETER)

<table>
<thead>
<tr>
<th>Resistance</th>
<th>Resolution</th>
<th>Accuracy % of reading + % of range</th>
<th>Current source</th>
</tr>
</thead>
<tbody>
<tr>
<td>100 ohm</td>
<td>100 uohm</td>
<td>0.010 + 0.004</td>
<td>1 mA</td>
</tr>
<tr>
<td>1 kohm</td>
<td>1 mohm</td>
<td>0.010 + 0.001</td>
<td>1 mA</td>
</tr>
<tr>
<td>10 kohm</td>
<td>10 mohm</td>
<td>0.010 + 0.001</td>
<td>100 uA</td>
</tr>
<tr>
<td>100 kohm</td>
<td>100 mohm</td>
<td>0.010 + 0.001</td>
<td>10 uA</td>
</tr>
<tr>
<td>1 Mohm</td>
<td>1 ohm</td>
<td>0.010 + 0.001</td>
<td>1 uA</td>
</tr>
<tr>
<td>10 Mohm</td>
<td>10 ohm</td>
<td>0.040 + 0.001</td>
<td>500 nA</td>
</tr>
<tr>
<td>100 Mohm</td>
<td>100 ohm</td>
<td>0.800 + 0.010</td>
<td>500 nA</td>
</tr>
</tbody>
</table>

Capacities :
- 36.000 pcs/hour